**Assignment No. 2**

**Digital Logic Design**

**Deadline: Monday 225th February 2019 (in the start of the class)**

**All Sections**

**Solve the following Problems from the textbook ‘Digital Design’ 5th Edition (also uploaded on slate).**

3.4, 3.6, 3.8, 3.9, 3.11, 3.12, 3.13, 3.15, 3.16, 3.19

Only attempt part a and b from every question mentioned above.